

[illegible]

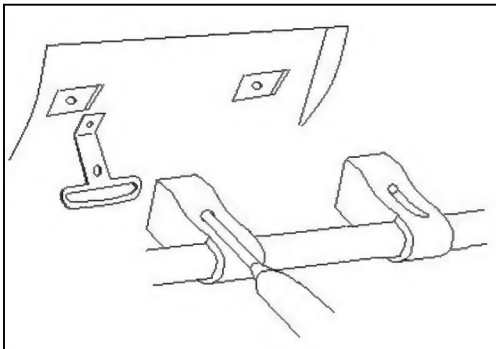
Diagram illustrating a 3D convolution operation. The input is a 3D volume of size 3x3x3, represented by three stacked 3x3 grids. A 3x3x3 kernel is applied to the input volume. The output is a 1x1x1 volume, represented by a single 1x1 grid.

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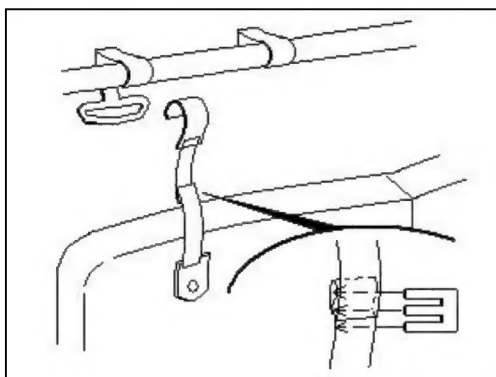
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Diagram illustrating the structure of a 32-bit register. The register is divided into four groups of 8 bits each, labeled 0-7, 8-15, 16-23, and 24-31. The first group (0-7) is highlighted with a red background. The second group (8-15) is highlighted with a blue background. The third group (16-23) is highlighted with a green background. The fourth group (24-31) is highlighted with a yellow background.



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